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First Named Inventor: Ide Yuuji
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FIG.1

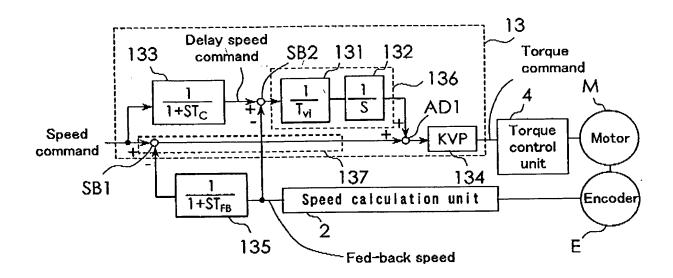


FIG.2

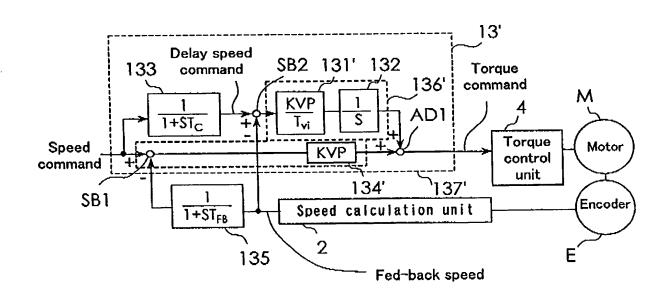


FIG.3

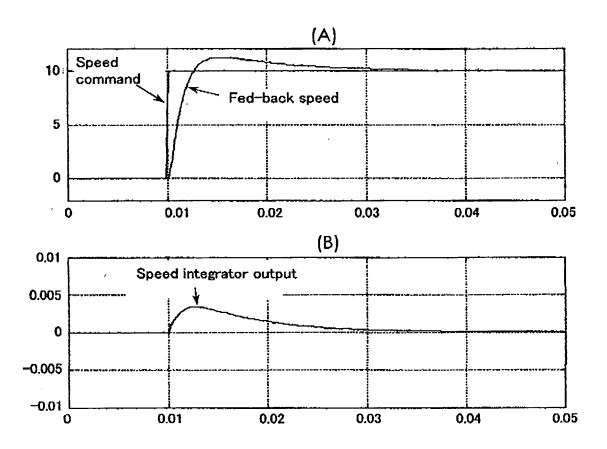
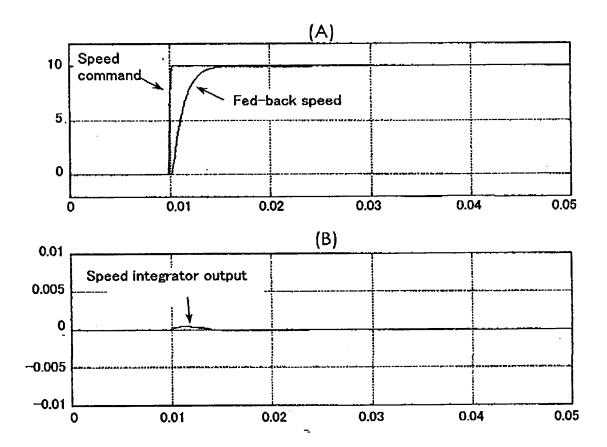


FIG.4



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FIG.5

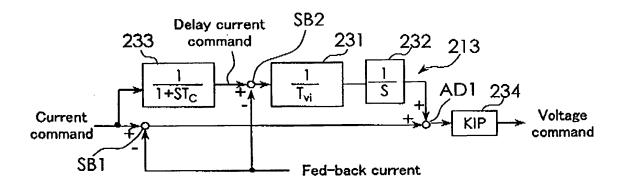


FIG.6

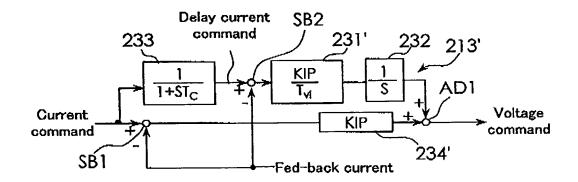
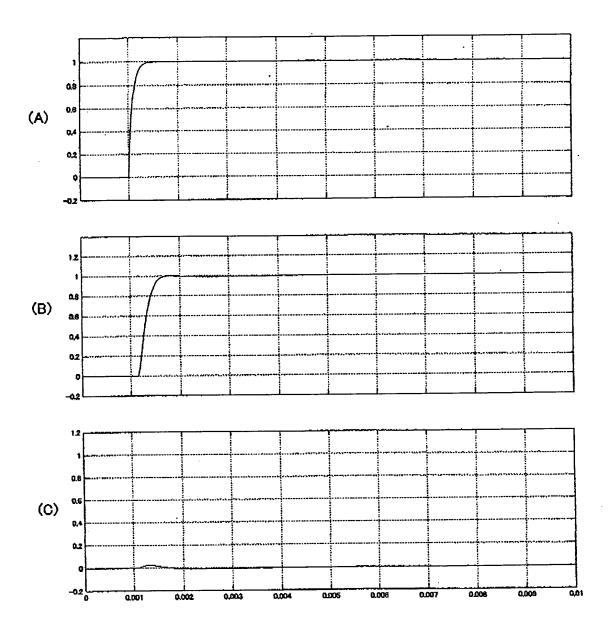


FIG.7



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FIG.8

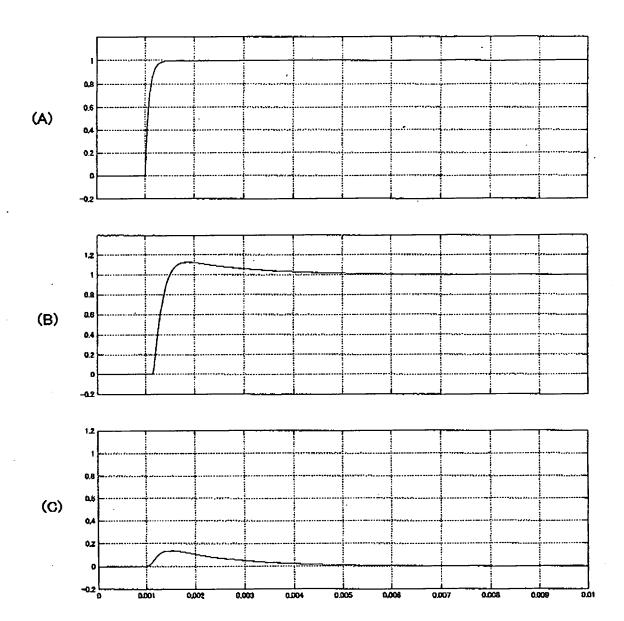
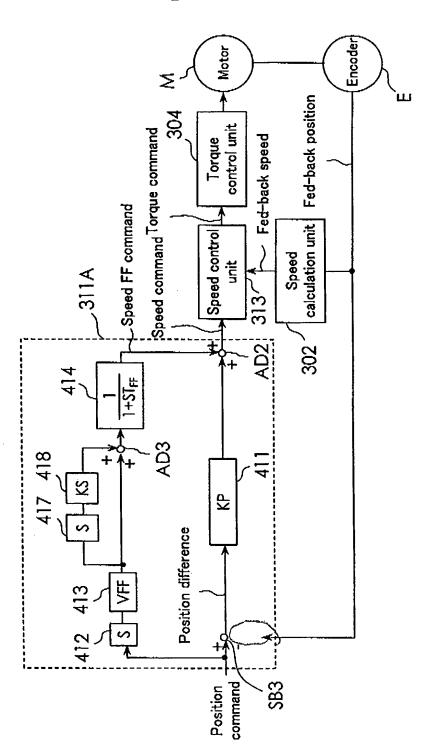
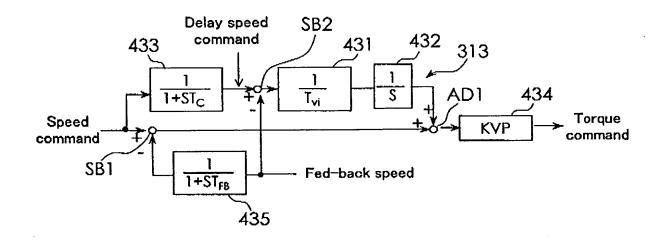


Fig.9



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FIG.10



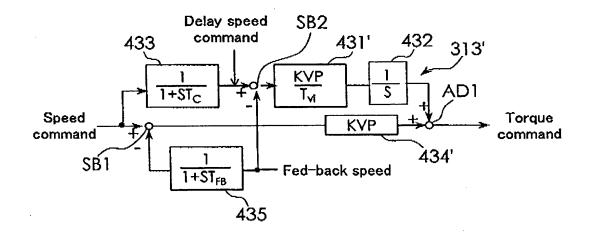


Fig.12

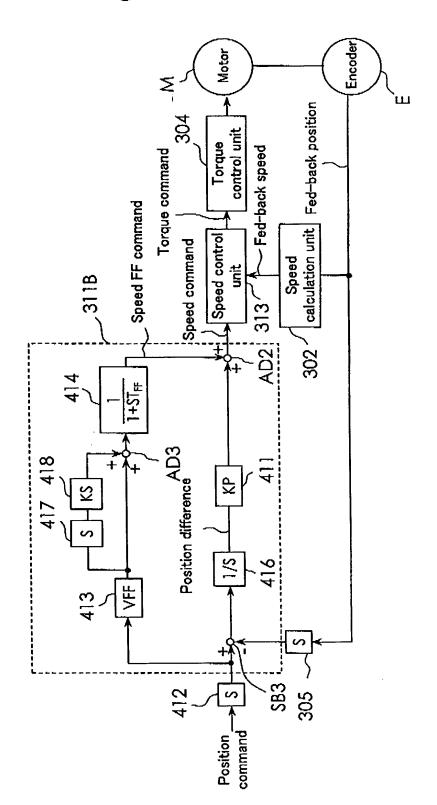


Fig.13

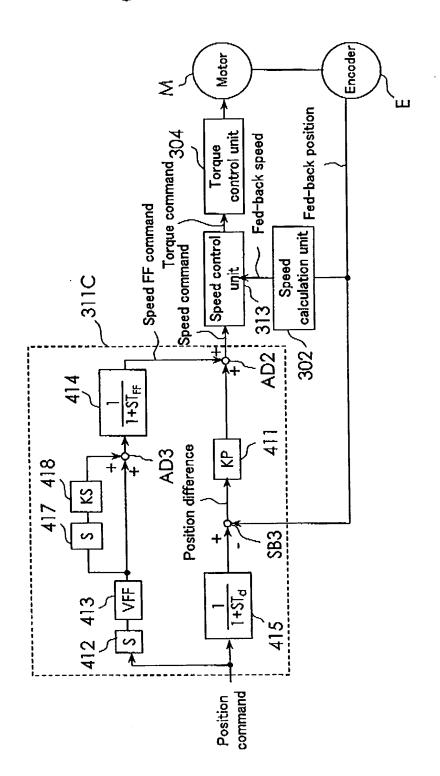
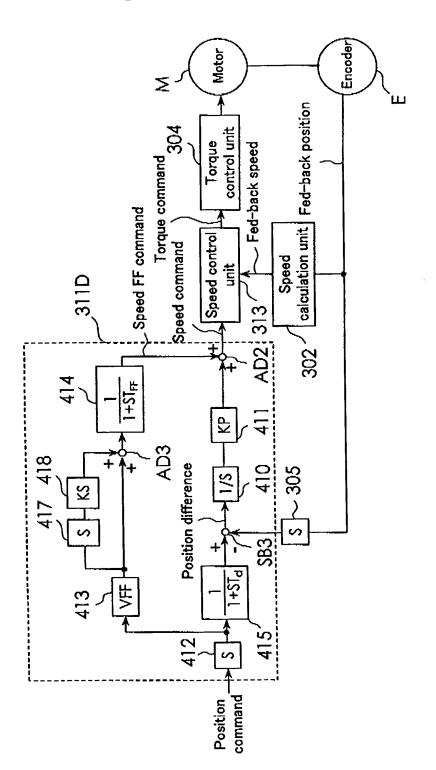
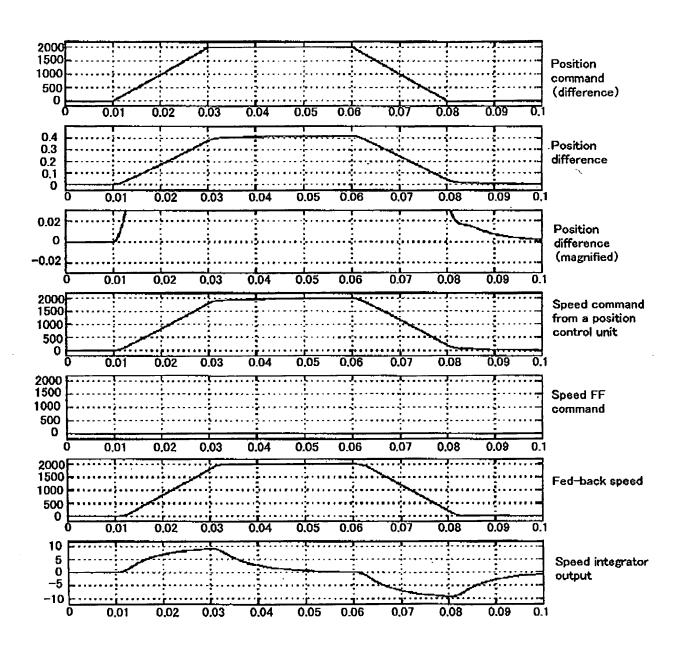


Fig.14



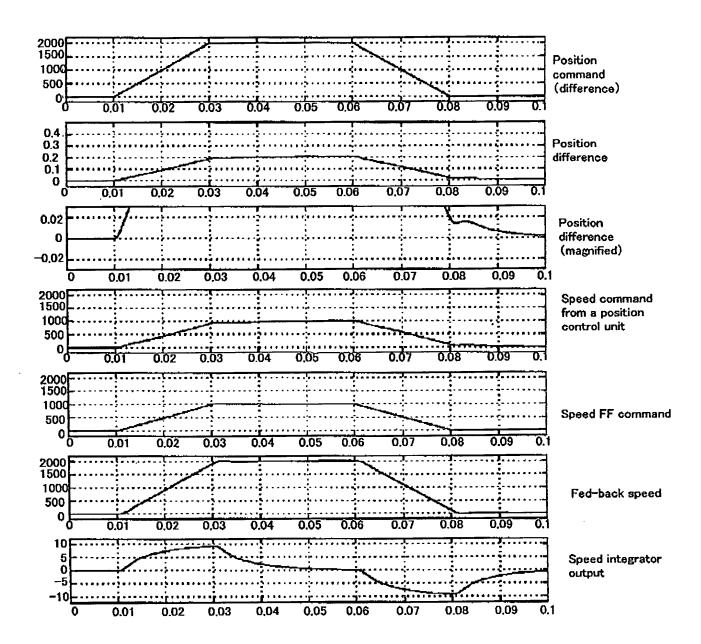
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FIG.15



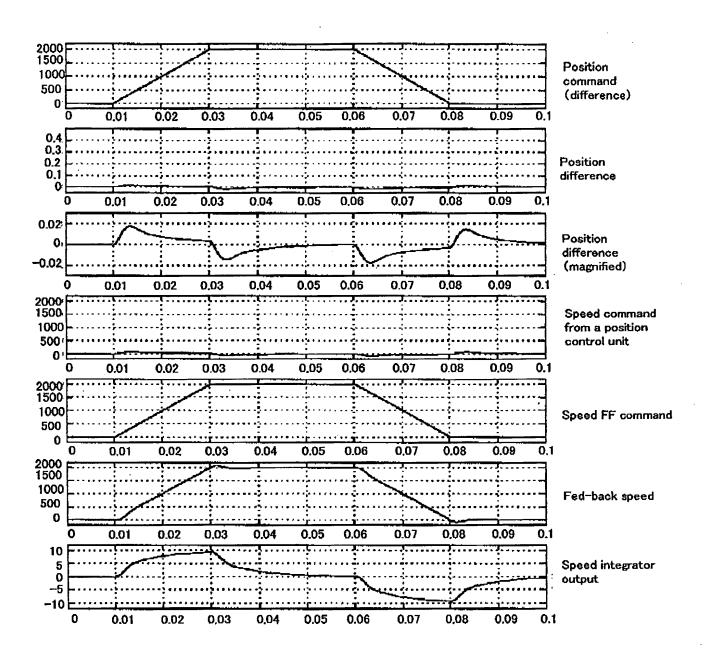
(Feed forward gain 0%)

FIG.16

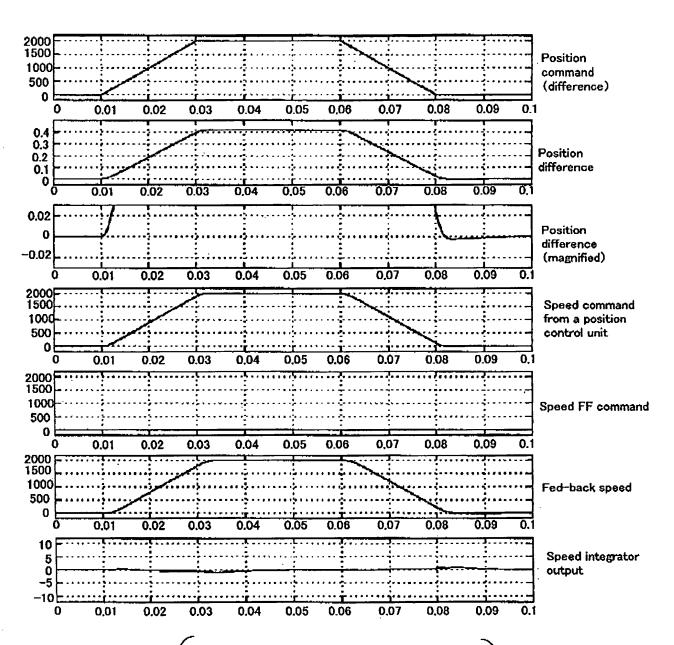


(Feed forward gain 50%)

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(Feed forward gain 100%)



Feed forward gain 0%
A delay compensation low-pass filter in the speed control unit is applied.

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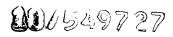
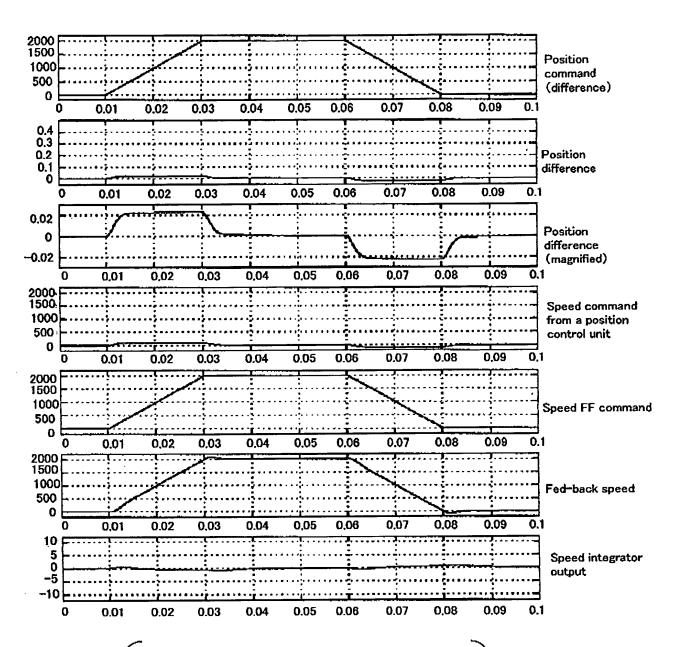
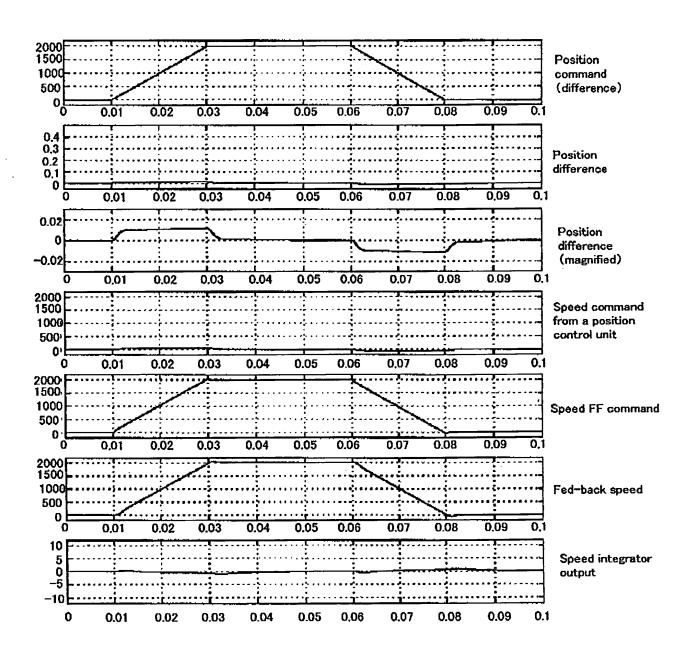


FIG.19



Feed forward gain 100%
A delay compensation low-pass filter in the speed control unit is applied.

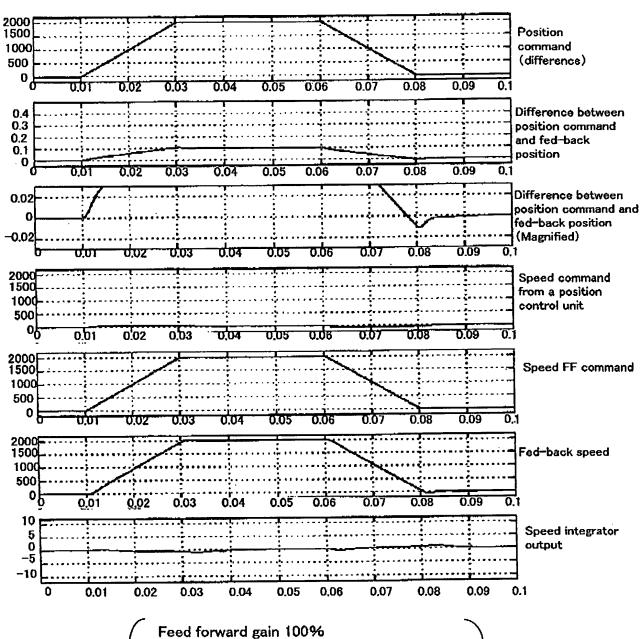
Differential gain 0



Feed forward gain 100%
A delay compensation low-pass filter in the speed control unit is applied.

Differential gain is set.

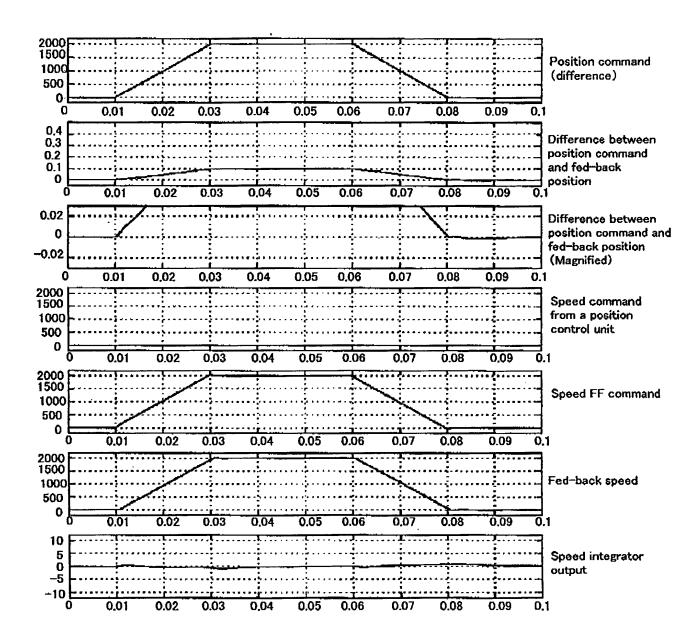
FIG.21



A delay compensation low-pass filter in the speed control unit is applied.

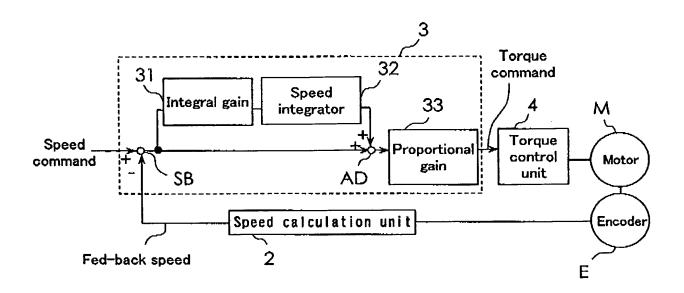
Differential gain 0

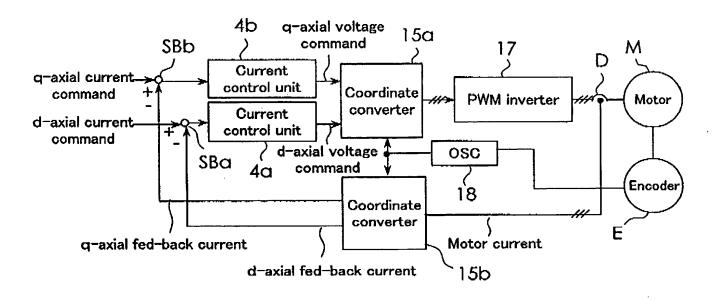
A delay compensation low-pass filter in position control unit is applied.

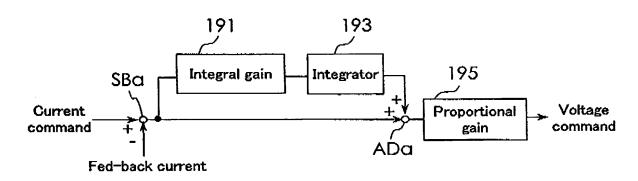


Feed forward gain 100%
A delay compensation low-pass filter in the speed control unit is applied.
Differential gain is set.
A delay compensation low-pass filter in position control unit is applied.

FIG.23







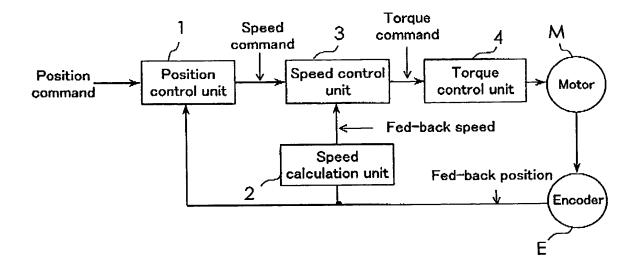


FIG.27

